

Receipt date: 09/29/2009

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10550326 - GAI: 2439

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10550326
	Filing Date		2005-09-23
	First Named Inventor	Roger D. Chamberlain	
	Art Unit	2439	
	Examiner Name	Tolentino, Roderick	
	Attorney Docket Number	53047-57365	

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	3	5440723		1995-08-08	Arnold et al.	
	4	5740244		1998-04-14	Indeck, et al.	
	5	5978801		1999-11-02	Yuasa	
	6	5995963		1999-11-30	Nanba et al.	
	7	6175874		2001-01-16	Imai et al.	
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9	6389532		2002-05-14	Gupta et al.	
10	6535868		2003-03-18	Galeazzi et al.	
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21	7225188	B1	2007-05-29	Gai et al.	
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	2	20020031125		2002-03-14	Sato	
	3	20020069370		2002-06-06	Mack	
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	2	2000357176	JP		2000-12-26	IBM		<input type="checkbox"/>
	3	2001014239	JP		2001-01-19	Hitachi Ltd		<input type="checkbox"/>
	4	2001518724	JP		2001-10-16	Worldtalk Corporation		<input checked="" type="checkbox"/>
	5	9-269930	JP		1997-10-14	Hitachi Ltd.		<input type="checkbox"/>
	6	1999055052	WO		1999-10-28	Sun Microsystems, Inc.		<input type="checkbox"/>
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	8	2001061913	WO		2001-08-23	Verimatrix, Inc.		<input type="checkbox"/>
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10	2002082271	WO		2002-10-17	Audible Magic Corporation	<input type="checkbox"/>
11	2003100662	WO		2003-12-04	Washington University	<input type="checkbox"/>
12	2004042560	WO		2004-05-21	Lockheed Martin Corporation	<input type="checkbox"/>
13	2004042561	WO		2004-05-21	Lockheed Martin Corporation	<input type="checkbox"/>
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2	"Technology Overview", downloaded from the http://www.datasearchsystems.com/tech.htm on April 19, 2004	<input type="checkbox"/>
3	ALTSCHUL et al., "Basic Local Alignment Search Tool", J. Mol. Biol., Oct. 5, 1990, 215, pp. 403-410	<input type="checkbox"/>
4	BEHRENS ET AL., "BLASTN Redundancy Filter in Reprogrammable Hardware," Final Project Submission, Fall 2003, Department of Computer Science and Engineering, Washington University	<input type="checkbox"/>
5	CAVNAR ET AL., "N-Gram-Based Text Categorization", Proceedings of SDAIR-94, 3rd Annual Symposium on Document Analysis and Information Retrieval, Las Vegas, pp. 161-175, 1994	<input type="checkbox"/>
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9	CHO ET AL., "Deep Packet Filter with Dedicated Logic and Read Only Memories", 12th Annual IEEE Symposium on Field-Programmable Custom Computing Machines, April 2004	<input type="checkbox"/>
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13	COMPTON ET AL., "Reconfigurable Computing: A Survey of Systems and Software", Technical Report, Northwestern University, Dept. of ECE, 1999, presented by Yi-Gang Tai	<input type="checkbox"/>
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15	DHARMAPURIKAR et al., "Deep Packet Inspection Using Parallel Bloom Filters," Symposium on High Performance Interconnects (HotI), Stanford, California, 2003, pp. 44-51	<input type="checkbox"/>
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23	International Preliminary Report on Patentability (Chapter II) for PCT/US2004/016021 dated August 21, 2007	<input type="checkbox"/>
25	International Preliminary Report on Patentability (Chapter II) for PCT/US2004/016398 dated March 13, 2006	<input type="checkbox"/>
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27	JEANMOUGIN et al., "Multiple Sequence Alignment with Clustal X", TIBS, 1998, Vol. 23, pp. 403-405	<input type="checkbox"/>
28	JONES et al., "A Probabilistic Model of Information Retrieval: Development and Status", Information Processing and Management, August 1998, 76 pages	<input type="checkbox"/>
29	JUNG ET AL., " Efficient VLSI for Lempel-Ziv Compression in Wireless Data Communication Networks", IEEE Transactions on VLSI Systems, Sept. 1998, pp. 475-483, Vol. 6, No. 3, Institute of Electrical and Electronics Engineers, Washington, DC, USA	<input type="checkbox"/>
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31	RAMAKRISHNA ET AL., "Efficient Hardware Hashing Functions for High Performance Computers", IEEE Transactions on Computers, December 1997, Vol. 46, No. 12	<input type="checkbox"/>
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33	RATHA ET AL., "FPGA-based coprocessor for text string extraction", IEEE, Sept. 11-13, 2000, pp. 217-221	<input type="checkbox"/>
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35	ROY, "A bounded search algorithm for segmented channel routing for FPGA's and associated channel architecture issues", IEEE, Nov. 11, 1993, pp. 1695-1705, Vol. 12	<input type="checkbox"/>
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